



MS AF
REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP

AF
JRW

AMENDMENT TRANSMITTAL LETTER

Docket No.
0951-0127P

Application No. 10/690,615-Conf. #5919	Filing Date October 23, 2003	Examiner J. M. Im	Art Unit 2811
---	---------------------------------	----------------------	------------------

Applicant(s): Hideya TAKAKURA

LEADFRAME FOR SEMICONDUCTOR DEVICE, METHOD FOR MANUFACTURING
Invention: SEMICONDUCTOR DEVICE USING THE SAME, SEMICONDUCTOR DEVICE USING THE
SAME, AND ELECTRONIC EQUIPMENT

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	10	- 20 =	0	x 50.00	0.00
Independent Claims	3	- 3 =	0	x 200.00	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

Large Entity

Small Entity

No additional fee is required for this amendment.

Please charge Deposit Account No. _____ in the amount of \$ _____.
A duplicate copy of this sheet is enclosed.

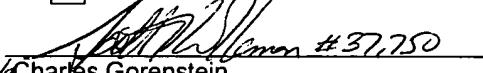
A check in the amount of \$ _____ is enclosed.

Payment by credit card. Form PTO-2038 is attached.

The Director is hereby authorized to charge and credit Deposit Account No. 02-2448
as described below. A duplicate copy of this sheet is enclosed.

Credit any overpayment.

Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.


Charles Gorenstein
Attorney Reg. No.: 29,271

Dated: January 25, 2007

BIRCH, STEWART, KOLASCH & BIRCH, LLP
8110 Gatehouse Road
Suite 100 East
P.O. Box 747
Falls Church, Virginia 22040-0747
(703) 205-8000



Docket No.: 0951-0127P
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Hideya TAKAKURA

Application No.: 10/690,615

Confirmation No.: 005919

Filed: October 23, 2003

Art Unit: 2811

For: LEADFRAME FOR SEMICONDUCTOR
DEVICE, METHOD FOR MANUFACTURING
SEMICONDUCTOR DEVICE USING THE
SAME, SEMICONDUCTOR DEVICE USING
THE SAME, AND ELECTRONIC
EQUIPMENT

Examiner: J. M. Im

RESPONSE TO FINAL ACTION UNDER 37 C.F.R. 1.116

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INTRODUCTORY COMMENTS

The following remarks are presented in response to the Office Action dated November 15, 2006.

Remarks/Arguments begin on page 2 of this paper.